Video IF Amplifier for Multistandard TV and VTR

Technology: Bipolar

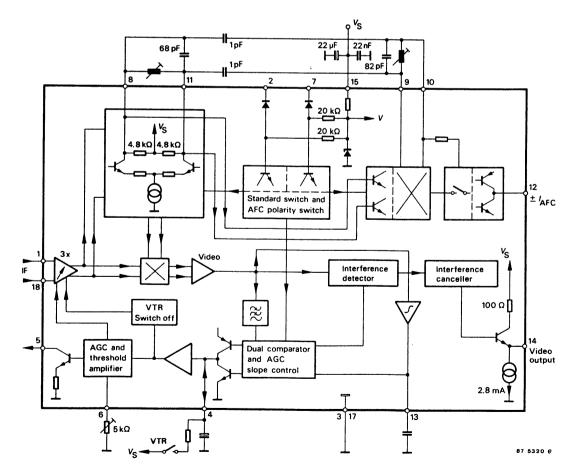
Features

- Standard B/G-L suitable, processes negatively and positively modulated IF-signals with equal polarity of the output signal
- Ultra white inverter and ultra black limiter for reducing transmission interference
- Internally noise protected gain control, no flyback pulses required
- Expanded video frequency response allows the demodulation of amplitude modulated MAC signals

- High input sensitivity
- Fast AGC by controlled discharge of the AGC capacitor
- Standard L mode: AGC acting on peak white level, capacitor discharge control by averaged video signal

Standard B/G: AGC acting on the sync. pulse peak

• The direction of the AFC curve is selectable independently from the standard switch

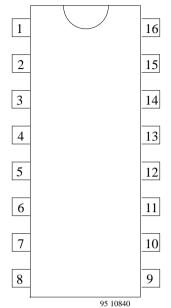


Case: DIP18

Figure 1. Block diagram

TDA4439

Pin Description



Pin	Function					
1, 18	IF input					
2	Standard switch: open B/G					
	ground L					
3, 17	Ground					
4	IF-AGC storage capacitor					
5	AGC (tuner control)					
6	Tuner AGC take over					
7	Polarity switch:					
	open AFC "DOWN"					
	ground AFC "UP"					
8, 11	Demodulator circuit					
9, 10	AFC circuit					
12	AFC output					
13	Average capacitor standard L					
14	Video output					
15	Supply voltage					
16	n.c.					

Circuit Description

The following function units are integrated in this circuit combination for video-IF processing:

- Three symmetric, highly stable, gain controlled wideband amplifier, quasi galvanic coupling eliminates feed back
- Video carrier controlled demodulator of high linearity
- Polarity switch over for video and AFC-signal
- Video output amplifier with low-pass characteristics, limiter for ultra black and inverter for ultra white interference
- Disconnectable AFC generator with push pull current output
- High impedance, interference free controlled voltage facilities, best possible AGC time constant with small storage capacitor
- Controlled discharge circuit for fast gain control
- With VTR operation the video output level is according to the ultra white level in B/G, ultra black level in L

Absolute Maximum Ratings

Reference point pin 3, unless otherwise specified

Parameters	Symbol	Value	Unit	
Supply voltage	Pin 15	VS	10 to 15	V
Supply current	Pin 15	IS	75	mA
Open loop voltage	Pin 5	V5	Vs	V
External voltage	Pin 4	V_4	10	V
	Pin 14	V ₁₄	8	V
Breaking current for VTR operation Pin 4		I ₄	0.5	mA
Video output current				
max load	Pin 14	I ₀	5	mA
short circuit max 1 s	Pin 14		50	
Power dissipation $T_{amb} \le 60$	P _{tot}	1.0	W	
Junction temperature		T _i	125	°C
Ambient temperature range		T _{amb}	-25 to +70	°C
Storage temperature range		T _{stg}	-25 to +125	°C

Thermal Resistance

Parameters	Symbol	Maximum	Unit
Junction ambient	R _{thJA}	60	K/W

Electrical Characteristics

 $V_S = 12 \text{ V}, T_{amb} = 25^{\circ}\text{C}$, Reference point Pin 3, unless otherwise specified

Parameters	Test Condition	ons / Pins	Symbol	Min.	Тур.	Max.	Unit
Supply current		Pin 15	IS		65		mA
Ultra white level at ¹⁾							
standard B/G		Pin 14	v ₀	4.8	5.2	5.6	V
Ultra black clamping level							
at standard B/G		Pin 14	v ₀	1.75	1.9	2.05	V
Composite video output ²⁾							
signal B/G	Peak to peak	Pin 14	v ₀	2.7	3.0	3.3	V
Video signal standard L ³⁾ (black/white)	Peak to peak	Pin 14	v ₀	1.85	2.1	2.35	v
Difference of the video							
signals standard L-B/G		Pin 14	Δ			10	%
Clamping level of black limiter		Pin 14	V ₁₄	250 mV below Sync. (typ.)		n)	
Threshold of the ultra white		1	• 14			(i) 2 j 10 (ij j	
inverter		Pin 14	V ₁₄	900 mV	V upper ulti	a white leve	el (typ.)
Grey level of the ultra white		1 111 1 1	• 14	>00 III			
inverter		Pin 14	V ₁₄		3.6		v
Supply voltage influence on							
the ultra black level in		-					
standard B/G		Pin 14	Δ		0.5		%/V
Supply voltage influence on							
the ultra white level in standard B/G		Pin 14	Δ		1.0		%/V
Video bandwidth	-3 dB	Pin 14	B _{video}		1.0		MHz
Video frequency response	-5 db	1 111 14	D _{v1deo}		10		IVIIIZ
over the AGC control range		Pin 14	ΔV_{video}			2.0	dB
Output DC current	$V_{14} = 8 V$	Pin 14	I ₁₄		2.8	2.0	mA
Response time of the peak ⁴	v ₁₄ = 0 v	1 111 14	-14				
white control in standard L		Pin 4	t _r			10	μs
Voltage level standard B/G ⁵		Pin 2	V ₂	2		V _S	V
Voltage level standard L ⁵⁾		Pin 2	V ₂	0		1.2	V
Input sensitivity (sym.) ⁶⁾	v ₁₄ =3.0 V _{pp} ,		• 2	Ŭ			
	$V_{14} = 0.8 V$	Pin 1–18	vi		120		μV
IF-AGC gain reduction			Δv_p	60			dB
Available tuner AGC							
10 dB via AGC use		Pin 5	I5	3	4		mA
Automatic tuner AGC with		— • –					
IF-control Pin 6 n.c.		Pin 5	AGC		61		dB

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ΤΕΜΙΟ

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Parameters	Test Conditions / Pins	Symbol	Min.	Тур.	Max.	Unit
IF-residual voltage at the	f = 38.9 MHz Pin 14	v		10		mV
video output in the AGC	f = 77.8 MHz Pin 14	4		20		
area						
Differential gain error	Pin 14	l d		3	5	%
Differential phase error	Pin 14	φ		3	5	degree
Sound-chroma beat (1.07	Video carrier $= 0 \text{ dB}$	α_{IM}		50		dB
MHz intermodulation) relat-	Pin 14					
ing to demodulated auxiliary	Colour carrier = -6 dB					
colour carrier	Sound carrier = -24 dB					
Upsetting factor sync. pulse		ΔV_{sync}		3		%
		V _{sync}				
Input impedance	Pin 1–18	R _i		1.6		kΩ
	Pin 1–18	B C _i		2		pF
Switch OFF voltage for						
VTR-operation	Pin 4	V_4	8		10	V
Switch OFF current for						
VTR-operation	Pin 4	I ₄			150	μΑ
DC voltage at the						
AFC circuit	Pins 9 and 10) V		5.0		V
Scope of the AFC voltage	Pin 12	2 V	1.0		V _S -1.5	V
AFC current	Pin 12	2 i ₁₂		0.8		mA
AFC transconductance	Pin 12	2 g		0.2		mA
						100kHz
AFC residual current	$V_{12} = V_S/2$ Pin 12	2 ±I _R			10	μΑ
(AFC "OFF")	12 5					
AFC current – OFF	Pins 9 and 10) I _{OFF}	100	150		μΑ
AFC polarity switching volt-	"AFC-up" Pin 7	V ₇	0		1.2	V
age ⁷⁾	"AFC-down"		2		Vs	

¹⁾ All measurements Pin 14 without load

²⁾ Residual carrier 10 %³⁾, Blanking level 30 % carrier amplitude

⁴⁾ A peak white value for at least $10 \,\mu s$ must be transmitted for each complete frame

⁵⁾ Direct control of standard reversing switch with TTL level

⁶⁾ Sync peak value standard B/G

7) AFC polarity switch may be directly matched to TTL-output (i.e. processor output)

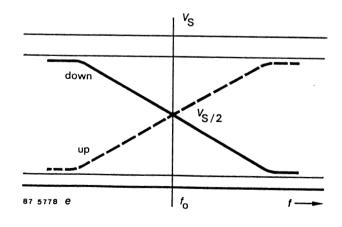


Figure 2. AFC characteristics/polarity

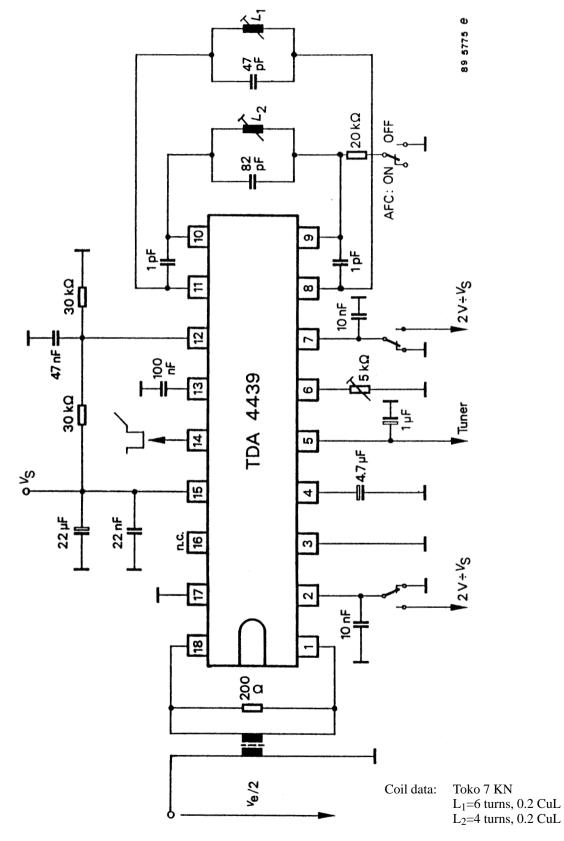
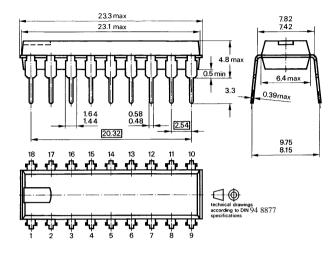


Figure 3. Test circuit

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Dimensions in mm

Package: DIP 16



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- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

TEMIC TELEFUNKEN microelectronic GmbH semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

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